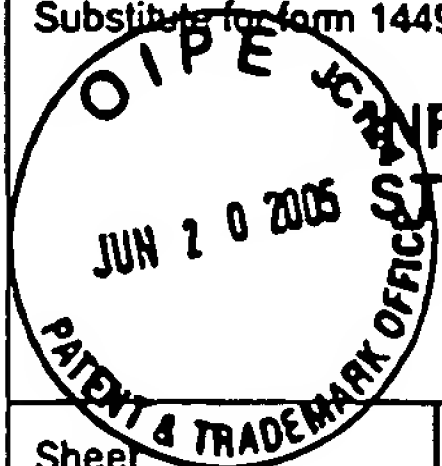


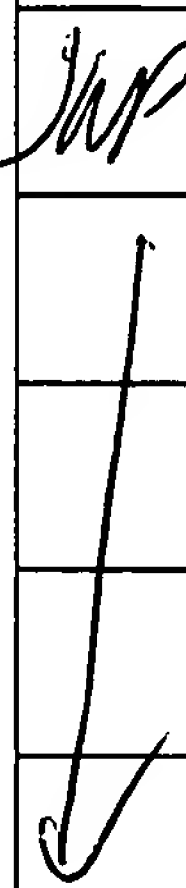


Substitute for form 1449-PTO <div style="text-align: center;">  INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> </div>				Complete if Known	
				Application Number	09/602279
				Filing Date	06/23/00
				First Named Inventor	Mario D. Nemirovsky
				Art Unit	2182
				Examiner Name	PEYTON, TAMMARA R
				Attorney Docket Number	MIPS.0168-00-US
Sheet	1	of	2		

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		4200927	4-29-1980	Huges et al.	
		6151644	11-21-2000	Chun-Chu Archie Wu	
		6453360	09-17-2002	Muller et al.	
		20010005253	06-28-2001	Hiroshi Komatsu	
		20020054603	05-2002	Mussoll et al.	
		20040213251	10-28-2004	Tran et al.	
		20040172504	09-02-2004	Balazich et al.	
		20040015598	01-22-2004	Jin-Fun, Lin	
		09/602,279	NA	Nemirovsky et al.	
			5559970	09-24-1996	Vinod Sharma
		6614796	09-02-2003	Black et al.	

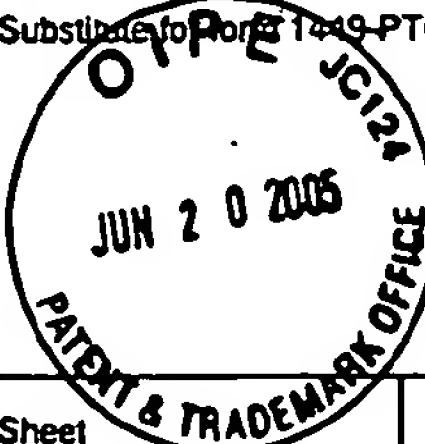
NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	AA	POTEL, M. J. "Real-Time Playback in Animation Systems." Proceedings of the 4 th Annual Conference on Computer Graphics and Interactive Techniques. 1977. pp. 72-77. San Jose, CA, US.
	AB	ARM Architecture Reference Manual. 1996. pp. 3-41, 3-42, 3-43, 3-67, and 3-68. Prentice Hall, NJ, US.
	AC	ESA/390 Principles of Operation. IBM Online Publications Center Reference Number SA22-7201-08. Table of Contents and paras. 7.5.31 and 7.5.70. IBM Corporation. Boulder, CO, US.
	AD	MC88110 Second Generation RISC Microprocessor User's Manual. 1991. pp. 10-66, 10-67, and 10-71. Motorola, Inc.
	AE	DIEFENDORFF ET AL. "Organization of the Motorola 88110 Superscalar RISC Microprocessor." IEEE Journal of Microelectronics. April 1992. pp. 40-63. Vol. 12, No. 2. IEEE. New York, NY, US.



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Substitution for Form 1449-PTO  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	09/602279
				Filing Date	06/23/00
				First Named Inventor	Marlo D. Nemirowsky
				Art Unit	2182
				Examiner Name	PEYTON, TAMMARA R
Sheet	2	of	2	Attorney Docket Number	MIPS.0168-00-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AF	KANE, GERRY. PA-RISC 2.0 Architecture. 1996, pp. 7-106 and 7-107. Prentice Hall. NJ, US.	
	AG	DIEFENDORFF ET AL. "Altivec Extension to PowerPC Accelerates Media Processing." IEEE Journal of Microelectronics. Vol. 20, No. 2 (2000): pp. 85-95	
	AH	GRUNEWALD ET AL. "Towards Extremely Fast Context Switching in a Block Multithreaded Processor." Proceedings of EUROMICRO 22, 1996. pp. 592-599	
	AI	BRADFORD ET AL. "Efficient Synchronization for Multithreaded Processors." Workshop on Multithreaded Execution, Architecture, and Compilation. Jan-Feb 1998. pp. 1-4	
	AJ	PAI ET AL. "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors." Proceedings of ASPLOS-VII, Oct. 1996: pp. 12-23, ACM, Inc.	
	AK	YOAZ ET AL. "Speculation Techniques for Improving Load Related Instruction Scheduling." 1999. Pages 42-53, IEEE.	
	AL	KESSLER, R. E. "The Alpha 21264 Microprocessor: Out-of-Order Execution at 600 MHz." August 1998.	
	AM	DONALSON ET AL. "DISC: Dynamic Instruction Stream Computer, An Evaluation of Performance." 26th Hawaii Conference on Systems Sciences. Vol. 1. 1993. Pages 448-456.	
	AN	NEMIROVSKY ET AL. "DISC: Dynamic Instruction Stream Computer." ACM. 1991. Pages 163-171	
	AO	MUSOLL ET AL. Mechanism to Prevent the Download of a Packet with Pending Writes Affecting Its Data. April 11, 2001. Disclosure Document #492430, USPTO	
	AP	UNGERER ET AL. A Survey of Processors with Explicit Multithreading. ACM Computing Surveys, Vol., 35, No. 1. March 2003. Pages 29-63.	

Examiner Signature		Date Considered	1/19/06, 1/20/06
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				Application Number	09/602279
				Filing Date	8/23/2000
				First Named Inventor	Marlo D. Nemirovsky
				Group Art Unit	2182
				Examiner Name	Tammara R. Peyton
				Attorney Docket Number	MIPS.0168-00-US
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2,3}			
JEP		09/691510	08-12-2000	Gellinas et al.	
JEP		6675790	10-07-1997	Kelth G. Wallis	
JEP		6389468	05-14-2002	Muller et al.	
JEP		6650840	11-18-2003	Muller et al.	

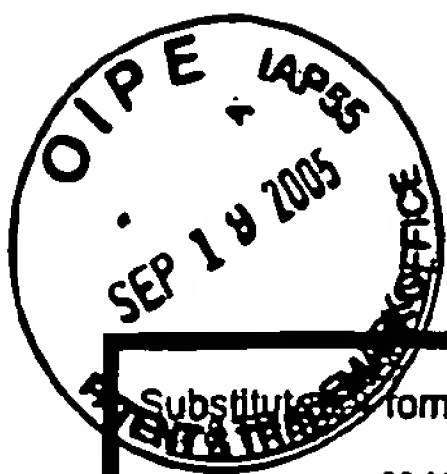
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ^o
		Country Code ³ - Number ⁴ - Kind Code ^{5,6}				

Examiner Signature		Date Considered	1/19/06
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/602279
				Filing Date	6/23/2000
				First Named Inventor	MARIO D. NEMIROVSKY
				Group Art Unit	2182
				Examiner Name	TAMMARA R. PEYTON
Sheet	1	of	1	Attorney Docket Number	MIPS.0168-00-US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
<i>MP</i>		6131163	10-10-2000	Scott L. Wiegel	
<i>MP</i>		6016308	1-18-2000	Crayford et al.	
<i>MP</i>		6502213	12-31-2002	Michel K. Bowman-Amuah	
<i>MP</i>		5408464	04-18-1995	Mark Jurkevich	

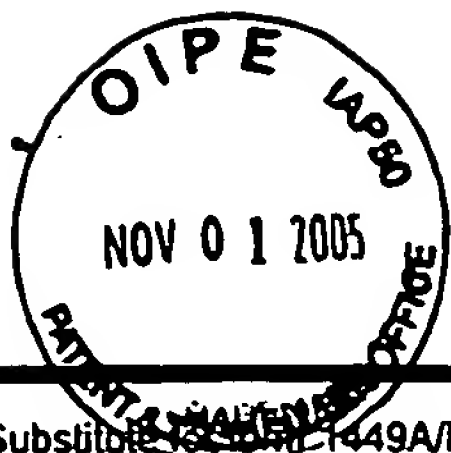
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

Examiner Signature	<i>Tammara R. Peyton</i>	Date Considered	1/19/06
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				Application Number	09/602279
				Filing Date	6/23/2000
				First Named Inventor	Mario D. Nemirovsky
				Group Art Unit	2182
				Examiner Name	TAMMARA R. PEYTON
Sheet	1	of	1	Attorney Docket Number	MIPS.0168-00-US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
JMP		20040148382	07-29-2004	Narad et al.	
JMP		20050061401	03-24-2005	Tokoro et al.	
JMP		20040172471	09-02-2004	Kelvin R. Porter	
JMP		20020124262	09-05-2002	Basso et al.	
JMP		6249801	06-19-2001	Zisapel et al.	
JMP		6219783	04-17-2001	Zahir et al.	
JMP		6253313	06-26-2001	Morrison et al.	
JMP		6047122	04-04-2000	Cynthia J. Spiller	
JMP		5892966	04-06-1999	Petrick et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

Examiner Signature		Date Considered	4/19/06
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